

# Intel® Atom™ Processor Launch SKUs

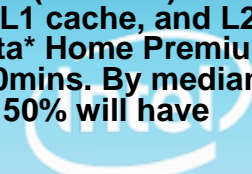
(Formerly Silverthorne processor)

No.	SKU	MHz	Power (W)			FSB Features	L2 Cache	Die Size	Kit (CPU+SCH) Pricing (per 1000 units)
			TDP Power <sup>1</sup>	Ave Power <sup>2</sup>	Idle Power (C6) <sup>3</sup>				
1.	Z500	800MHz	0.65W	160mW <sup>2</sup>	80mW	400MHz	512K	7.8mm x 3.1mm	\$45
2.	Z510	1.1GHz	2W	220mW <sup>2</sup>	100mW	400MHz			\$45
3.	Z520	1.33GHz	2W	220mW <sup>2</sup>	100mW	533MHz HT			\$65
4.	Z530	1.6GHz	2W	220mW <sup>2</sup>	100mW	533MHz HT			\$95
5.	Z540	1.86GHz	2.4W	220mW <sup>2</sup>	100mW	533MHz HT			\$160

1: TDP specification should be used to design the processor thermal solution. Thermal solutions not designed to provide this level of thermal capability may affect the long-term reliability of the processor and system.

2: Average power is defined as measured CPU power whilst running BAPCo MobileMark'05 Office Productivity suite on Microsoft\* Windows\* XP for a period of 90min at 50°C . Assumes top bin frequency, median leakage, Adaptive mode. By median leakage device it means that 50% of the CPUs will have leakage values below the median value and 50% will have leakage values above the median.

3: Idle power on the Intel Atom processor is defined as the power in the Intel Deep Power Down state (C6 state). Intel Deep Power Down Technology (C6) is the lowest power state of the CPU when the core clock, PLL, L1 cache, and L2 cache are off. Z510, Z520, Z530, Z540 is measured while running Idle under Microsoft\* Windows Vista\* Home Premium for a period of 30mins. Z500 is measured while running Idle under MIDINUX Linux\* for a period of 30mins. By median leakage device it means that 50% of the CPUs will have leakage values below the median value and 50% will have leakage values above the median.



**Under Embargo until April 1, 8:30pm US pacific time**